

## AMENDMENTS TO THE CLAIMS

*The listing of claims will replace all prior versions and listings of claims in the application:*

### **Listing of Claims:**

1. **(Currently Amended)** An assembly of a transmitter optical assembly, a receiver optical assembly and a host, comprising:

[[a]]the transmitter optical assembly comprisingincluding:

a transmitter substrate that includes a transmitter power line and a transmitter conductive path coupled to a transmitter optical assembly connector;

a laser source mounted on the transmitter substrate; and

a[[n]] transmitter integrated circuit mounted on the transmitter substrate, comprisingincluding:

a transmitter substrate that includes a power line and a conductive path;

a laser source mounted on the transmitter substrate; and

a laser control mounted on the transmitter substrate, the laser control communicably connected with one or more of the laser source, the transmitter power line, and the transmitter conductive path, the laser control comprisingincluding a transmitter memory portion, the transmitter memory portion including one or more memory components for receiving or storing data[[.]].

the receiver optical assembly including:

a receiver substrate that includes a receiver power line and a receiver conductive path coupled to a receiver optical assembly connector;

a photodiode mounted on the receiver substrate; and

a receiver integrated circuit including:

a processing control communicably connected with one or more of the photodiode, the receiver power line, and the receiver conductive path, the processing control including a receiver memory portion,

the receiver memory portion including one or more memory components for receiving or storing data; and

the host including:

a host CPU;

transmitter specific conductive lines configured to communicate electrical signals with the transmitter optical assembly; and

receiver specific conductive lines configured to communicate electrical signals with the receiver optical assembly.

2. **(Currently Amended)** The ~~transmitter-optical~~ assembly as recited in claim 1, wherein the laser control further ~~comprises~~includes a laser modulator, the laser modulator administering an alternating current from the laser control to the laser source; and a laser bias, the laser bias administering a direct current from the laser control to the laser source.
3. **(Currently Amended)** The ~~transmitter-optical~~ assembly as recited in claim 1, wherein the transmitter substrate further ~~comprises~~includes connections for one or more of a ground line, a diagnostic data line, and a diagnostic clock.
4. **(Currently Amended)** The ~~transmitter-optical~~ assembly as recited in claim 1, wherein the transmitter conductive path is a transmission line that carries data from the host to the transmitter optical assembly, wherein the data are ultimately transmitted at the laser source.
5. **(Currently Amended)** The ~~transmitter-optical~~ assembly as recited in claim 1, wherein the transmitter substrate comprises ceramic materials, and wherein circuit traces on the ceramic materials include three-dimensional metallic sputtering to shield electromagnetic interference.
6. **(Currently Amended)** The ~~transmitter-optical~~ assembly as recited in claim 1, the transmitter optical assembly further ~~omprising~~including a monitor photodiode, the monitor photodiode communicably connected with the laser source and the laser control, the monitor photodiode providing the laser control with status information about the laser source.
7. **(Currently Amended)** The ~~transmitter-optical~~ assembly as recited in claim 1, wherein the transmitter memory portion comprises one or more of an EEPROM, and a RAM.

8. **(Currently Amended)** The ~~transmitter-optical~~-assembly as recited in claim 7, wherein at least one of the one or more memory components of the transmitter memory portion includes a portion that stores one or more of status and fault information, and operating temperature information.

9. **(Currently Amended)** The ~~transmitter-optical~~-assembly as recited in claim 7, wherein at least one of the one or more memory components of the transmitter memory portion includes a portion for receiving diagnostic data.

10. **(Canceled)**

11. **(Currently Amended)** The ~~receiver-optical~~-assembly as recited in claim 1[[0]], wherein the processing control further ~~comprises~~includes a temperature sensor.

12. **(Currently Amended)** The ~~receiver-optical~~-assembly as recited in claim 1[[0]], wherein the ~~optical-converter~~is photodiode is an avalanche photo-diode, the receiver optical assembly further ~~comprising~~including a bias control mounted on the receiver substrate, the bias control communicatively connected with the ~~optical-converter~~avalanche photo-diode, and the processing control.

13. **(Currently Amended)** The ~~receiver-optical~~-assembly as recited in claim 1[[0]], wherein the receiver conductive path is a data receiving line that carries data from the receiver optical assembly to the host.

14. **(Currently Amended)** The ~~receiver-optical~~-assembly as recited in claim 1[[0]], wherein the receiver substrate comprises ceramic materials, and wherein circuit traces on the ceramic materials include three-dimensional metallic sputtering to shield electromagnetic interference.

15. **(Currently Amended)** The ~~receiver-optical~~-assembly as recited in claim 1[[0]], wherein at least one of the one or more memory components of the receiver memory portion ~~comprise~~include one of an EEPROM, and a RAM.

16. **(Currently Amended)** The ~~receiver-optical~~-assembly as recited in claim 15, wherein at least one of the one or more memory components of the receiver memory portion includes a portion for receiving and storing diagnostic data.

17. **(Currently Amended)** The ~~receiver-optical~~ assembly as recited in claim 16, wherein the receiver substrate further comprises connections for one or more of a ground line, a diagnostic data line, and a diagnostic clock.

18. **(Canceled)**

19. **(Canceled)**

20. **(Canceled)**

21. **(Canceled)**

22. **(Canceled)**

23. **(Canceled)**

24. **(Canceled)**

25. **(Canceled)**

26. **(Canceled)**

27. **(Canceled)**

28. **(Canceled)**

29. **(Canceled)**

30. **(Currently Amended)** An optical transceiver comprising ~~a fiber optic subassembly operably attached to the optical transceiver, the fiber optic subassembly comprising:~~

a combination transmitter and receiver substrate including a power line, at least one data transmission line, and at least one data reception line configured to connect to a host;

a laser source mounted on the combination transmitter and receiver substrate;

a photo detector mounted on the combination transmitter and receiver substrate; and

a[[n]] control integrated circuit mounted on the combination transmitter and receiver substrate comprising including:

at least one of a transmitter and/or a receiver;

a combination transmitter/receiver substrate including a power line and a conductive path; and

datameans operably disposed within the fiber optic subassembly for communicating high frequency optical data, wherein the means for communicating optical data includes

a modulator configured to provide adjustable current to the laser source to transmit electrical data received from the host to the laser source;

a post amplifier configured to receive received electrical data from the photo detector and amplify the received electrical data signal before relaying the electrical data signal to the host computer along the at least one data reception line; and

a processor configured to receive diagnostic data from the host computer, the processor being one or more conventional optical transceiver components within the fiber optic subassembly incorporated in thea common integrated circuit along with the modulator and post amplifier, such that impedance that would otherwise be present in a high frequency electronic data communication is minimized due to the integration of the transeiver componentsmodulator, post amplifier, and processor in a common integrated circuit.

31. **(Canceled)**

32. **(Currently Amended)** The ~~transmitter optical integrated circuit assembly~~ as recited in claim 1, wherein the laser control is connected directly to the laser source.

33. **(Currently Amended)** The ~~transmitter optical integrated circuit assembly~~ as recited in claim 1, further comprising a bond wire directly connecting the laser control to the laser source.

34. **(Currently Amended)** The ~~receiver optical integrated circuit assembly~~ as recited in claim 1[[0]], wherein the processing control is directly connected to the photodetector.

35. **(Currently Amended)** The ~~receiver optical integrated circuit assembly~~ as recited in claim 1[[0]], further comprising a transimpedance amplifier and a bond wire, wherein the photo detector is directly connected to the transimpedance amplifier or processing control by the bond wire.

36. **(Currently Amended)** An integrated circuit comprising:  
an integrated circuit substrate that includes a power line and a conductive path;  
a laser driver ~~omprising~~including a current source for driving a laser;  
[[or ]]a post amplifier for amplifying a signal received form a photodetector; and  
control circuitry including laser control [[or]]and processing control, the control circuitry  
mounted on the integrated circuit substrate, the control circuitry directly  
connected to the laser source [[or]] and to the photodiode, the control circuitry  
including a memory portion, the memory portion including one or more memory  
components for receiving [[or]]and storing data.
37. **(Currently Amended)** The integrated circuit as recited in claim 36[[4]], wherein  
the control circuitry further includes a transimpedance amplifier.
38. **(Currently Amended)** The integrated circuit as recited in claim 36[[4]], wherein  
the integrated circuit comprises a laser source and a photodiode, and wherein the control  
circuitry is directly connected to the laser source and the photodiode.
39. **(Canceled)**
40. **(New)** The system of claim 1, wherein:  
the transmitter optical assembly connector includes an edge connector for electrically  
connecting to the transmitter specific conductive lines of the host;  
the receiver optical assembly further includes an edge connector for electrically  
connecting to the receiver specific conductive lines of the host; and  
the host includes:  
a transmitter optical assembly receptacle for receiving the transmitter optical  
assembly's edge connector; and  
a receiver optical assembly receptacle for receiving the receiver optical  
assembly's edge connector.
41. **(New)** The system of claim 1, wherein the laser control primarily uses analog signaling.

42. **(New)** The system of claim 41, wherein the transmitter optical assembly does not store digital diagnostic data in the memory portion and no matching network is present between the transmit integrated circuit and the laser source.
43. **(New)** The optical transceiver of claim 30, further comprising a transimpedance amplifier (TIA) mounted on the combination transmitter and receiver substrate, the integrated circuit further including a TIA supply for supplying power to the TIA.
44. **(New)** The optical transceiver of claim 43, wherein the photo detector includes an avalanche photodiode (APD) and the integrated circuit further includes APD bias command control circuitry for sending commands to an APD regulator circuit mounted on the combination transmitter and receiver substrate.
45. **(New)** The optical transceiver of claim 30, wherein the processor is configured to set limits on power used by the laser source and the photodetector.
46. **(New)** The optical transceiver of claim 30, wherein the integrated circuit includes a sense and control component that communicates with a monitor photo-diode to provide status information to the processor regarding the laser source.